



#### **Features**

- 1250Mbps Typical Data Rate and compliant to 1000Base BX20 IEEE802.3ah
- PIN photo-detector
- Up to 20km on 9/125µm SMF
- Hot-pluggable CSFP footprint
- LC/UPC type pluggable optical interface
- Achieve operational compatibility with conventional SFP
- Metal enclosure, for lower EMI
- RoHS compliant and lead-free
- Single +3.3V power supply
- Support Digital Diagnostic Monitoring interface
- Compliant with SFF-8472
- Case operating temperature 0°C to +70°C

#### **Ordering Information**

PART NUMBER	INPUT/OUTPUT	MONITOR	VOLTAGE	TEMPERATURE
CL-CSFP-WDM-20-31 DD	AC/AC	Yes	3.3V	$0^{\circ}$ C to $70^{\circ}$ C
CL-CSFP-WDM-20-49 DD	AC/AC	Yes	3.3V	$0^{\circ}$ C to $70^{\circ}$ C

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# I. Pin Descriptions

Pin#	Name	Function	Notes	
4	\/⊏⊏	Transacione Cravad	VEE may be internally connected within	
1	VEE	Transceiver Ground	the SFP module	
			TX Fault is an open collector/drain output,	
2	TX FAULT	Transmitter Fault	which should be pulled up with a	
_	IXIXOLI	Indication	4.7K–10K resistor on the host board. Note	
			1 for more information	
3	TX1_	Transmitter Disable of Ch	Module channel A disables function	
	Disable	A		
4	MOD-DEF2	Two-wires interface Data	2 wire serial ID interface, SDA	
5	MOD-DEF1	Two-wires interface Clock	2 wire serial ID interface, SCL	
6	TD2-	Inverted Transmit Data	These are the differential transmitter puts.	
0	102-	Input of Ch B	They are AC-coupled, differential lines	
			with 100 differential termination inside the	
7	TD2+	Transmit Data Input of Ch	module. The AC coupling is done inside	
7		В	the module and is thus not required on the	
			host board	
8	LOS1	Loss of Signal of Ch A	Loss of Signal detected function. Note 2	
0	LOST	LOSS OF Signat of CITA	for more information.	
		Received Data Output of	These are the differential receiver outputs.	
9	RD2+	Ch B	They are AC coupled 100 differential lines	
			which should be terminated with	
		Inverted Received	100(differential) at the user SERDES. The	
10	RD2-	Data Output of Ch B	AC coupling is done inside the module	
		2 ata Catpat of Off B	and is thus not required on the host board.	
11	VEE	Transcoiver Oracinal	VEE may be internally connected within	
11	VEE	Transceiver Ground	the SFP module.	

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Pin#	Name	Function	Notes		
12	RD1-	Inverted Received	These are the differential receiver outputs.		
12	KD1-	Data Output of Ch A	They are AC coupled 100 differential lines		
			which should be terminated with		
13	RD1+	Received Data Output of	100(differential) at the user SERDES. The		
13	KDIT	Ch A	AC coupling is done inside the module		
			and is thus not required on the host board.		
14	LOS2	Loss of Signal of CH B	Loss of Signal detected function. Note 2		
1-7	L002	L033 of Signal of Cit B	for more information.		
15	VCCR	Receiver Power	3.3V± 5%. Note 3 for more information		
16	VCCT	Transmitter Power	3.3V± 5%. Note 3 for more information		
17	TX2_	Transmitter Disable of	Module channel B disables function		
17	Disable	Ch B			
18	TD1+	Transmit Data Input of	These are the differential transmitter puts.		
10	101+	Ch A	They are AC-coupled, differential lines		
			with 100 differential termination inside the		
10	TD1-	Inverted Transmit Data	module. The AC coupling is done inside		
19		Input of Ch A	the module and is thus not required on the		
			host board		
20	VEE	Transpoiver Cround	VEE may be internally connected within		
20	VEE	Transceiver Ground	the SFP module.		

Note 1: When high, output indicates a laser fault of some kind either in Channel A or Channel B. The Host shall read Channel A/B for details: TX Fault from channel A if bit 2 is set in [A2H:110]; TX Fault from channel B if bit 2 is set in [B2H: 110]. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

Note 2: When high, this output indicates the received optical power is below the worst-case

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receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.4V.

Note 3: VccT VccR are the power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 400Ma@3.3V. Vcc may be internally connected within the SFP transceiver module.

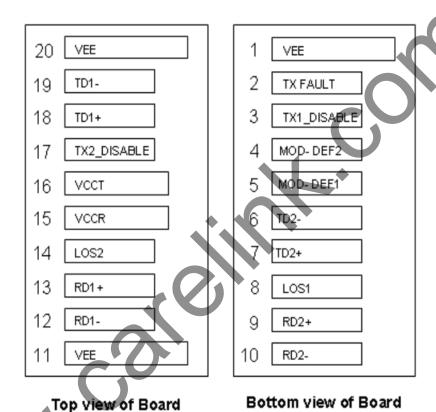


Figure 2: Pin out of Connector Block on Host Board

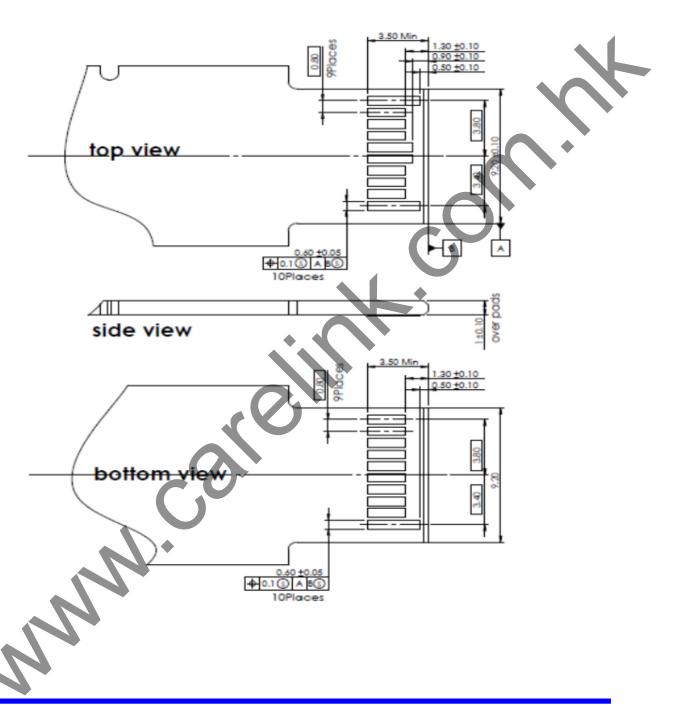
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(A siview through top of board)





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### **II.** Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Storage Temperature	Ts	-40		85	°C	
Storage Ambient Humidity	На	5		95	%	
Power Supply Voltage	Vcc	-0.5		4	V	
Signal Input Voltage		-0.3		Vcc+0.3	V	
Receiver Damage Threshold		3			dBm	

# III. Recommended Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	Tcase	0		70	°C	
Ambient Humidity	На	5		70	%	Non-condensing
Power Supply Voltage	Vcc	3.13	3.3	3.47	V	
Power Supply Current	Icc	1X		450	mA	
Power Supply Noise Rejection				100	mVp-p	100Hz to 1MHz
Data Rate			1.25/1.25		Gbps	TX Rate/RX Rate
Transmission Distance				20	KM	
Coupled Fiber	Single mode fiber					9/125um SMF

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### IV. Specification of Transmitter

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Average Output Power	Роит	-9		-3	dBm	
Extinction Ratio	ER	8			dB	
Contor Wayalangth	<b>)</b> -	1270	1310	1360	nm	3149
Center Wavelength	λο	1470	1490	1510	nm	4931
Chartrum Width (DMC)	~			3.5	nm	FP Laser
Spectrum Width (RMS)	σ			3.5	nm	(TX:1310nm)
Side Mode Suppression Ratio	SMSR	30			dB	DFB Laser
Spectrum Bandwidth(-20dB)	σ			1	nm	(TX:1490nm)
Transmitter OFF Output Power	Poff			-45	∲dBm	
Differential Line Input Impedance	RIN	90	100	110	Ohm	
Jitter P-P	t∍	<b>*</b>		128	ps	Note (1)
Output Eye Mask	Compliant	with IEEE	802.3 ah	(class 1		Note (2)
		laser sa	fety)			14016 (2)

Note (1): Measure at 2^7-1 NRZ PRBS pattern

Note (2): Transmitter eye mask definition, and eye mask diagram with at least 10% margin.

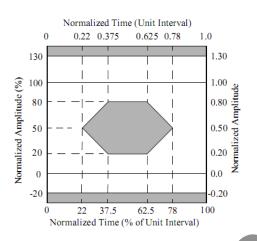
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# V. Specification of Receiver

Parameter	Symb ol	Min.	Тур.	Max.	Unit	Note
Input Optical Wayalanath	<b>λ</b>	1470	1490	1510	nm	
Input Optical Wavelength	λім	1270	1310	1360	nm	
Receiver Sensitivity	Pin			-19	dBm	Note (1)
Input Saturation Power (Overload)	Psat	-3			dBm	
Los Of Signal Assert	PA	-38			dBm	
Los Of Signal De-assert	Pb			-22	dBm	Note (2)
LOS Hysteresis	Pa-Pd	0.5	2	6	dB	

Note (1): Measured with Light source 1490nm(1310nm), ER=8dB; BER =<10^-12 @PRBS=2^7-1 NRZ

Note (2): When LOS de-asserted, the RX data+/- output is High-level (fixed)

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### VI. Electrical Interface Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Transmitter	1					
Total Supply Current	Icc			А	mA	Note (1)
Transmitter Disable Input-High	VDISH	2		Vcc+0.3	V	<b>^</b>
Transmitter Disable Input-Low	VDISL	0		0.8	V	
Transmitter Fault Input-High	VDISL	2		Vcc+0.3	V	
Transmitter Fault Input-Low	VTxFH	0		0.8	V	
Receiver						
Total Supply Current	Icc			В	mA	Note (1)
LOSS Output Voltage-High	VLOSH	2		Vcc+0.3	V	I.V.TTI
LOSS Output Voltage-Low	VLOSL	0		0.8	V	LVTTL

Note (1): A (TX) + B (RX) = 450mA (Not include termination circuit)

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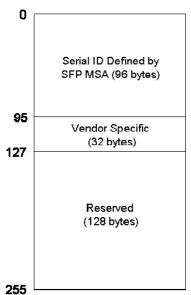
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### VII. Digital Diagnostic Functions

The digital diagnostic memory is defined as follow:

Channel 1: 2 wire address 1010000X (A0h) Channel 2: 2 wire address 1011000X (B0h)



Channel 1: 2 wire address 1010001X (A2h) Channel 2: 2 wire address 1011001X (B2h)

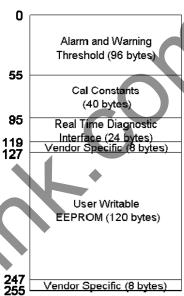


Figure 29 Memory map of 2ch Compact SFP (option 2)

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The operating and diagnostics information is monitored and reported by a Digital Diagnostics

Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

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### VIII. Recommend Circuit Diagram

#### **Carelink CSFP Module Host Board** RD1+ 0,1μ Ή<del>ς</del> ∏100Ω RD1--П-С 0.1µ Receiver Data 4.7~10kΩ LOS1 0.1μ CH1 11 C TD1+ 100 🛱 <sup>11</sup>С 0.1µ TD1-TX1\_DISABLE Transimit Data -10kΩ 4.7~10 κΩ TX FAULT 7~10kΩ MOD\_DEF1 EEPROM MOD\_DEF2 Note1 0.1μ TD2+ "c 100Ω C Transimit Data 0.Τμ TX2\_DISABLE Note1 100Ω 4.7~10kΩ 052 CH<sub>2</sub>

RD2+

RD2-

**Note1** Recommendation  $100\Omega$  series resistance on host board.

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Receiver Data

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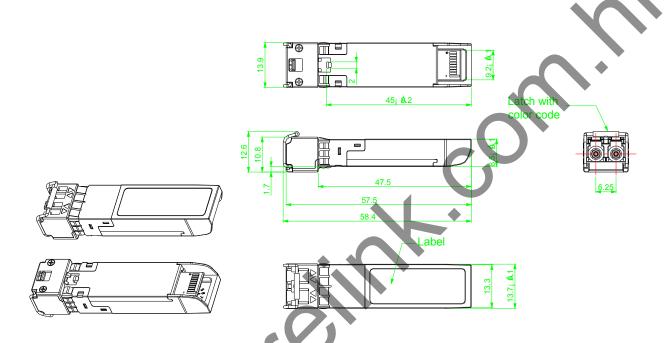
0.1µ П<sub>С</sub>

0.1μ

100Ω



### IX. Mechanical Specifications (Unit: mm)



# X. Regulatory Compliance

Feature	Reference	Performance	
Electrostatic discharge ( ESD )	IEC/EN 61000-4-2	Compatible with standards	
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022	Composible with standards	
Electromagnetic Interiererice (Elvir)	Class B (CISPR 22A)	Compatible with standards	
ROHS	2002/95/EC	Compatible with standards	
EMC	EN61000-3	Compatible with standards	

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# RoHS compliant

2X TX-1310/RX-1490 nm or 2X TX-1490/RX-1310nm Single-mode Bi-directional /Compact Small Form Pluggable (SFP), 3.3V 1.0625Gbd Fiber Channel/1.25 Gigabit Ethernet

#### **Eye Safety Mark**

The SFP series singlemode transceiver is a class 1 laser product. It complies with EN 60825-1 and FDA 21 CFR 1040.10 and 1040.11. In order to meet laser safety requirements the transceiver shall be operated within the Absolute Maximum Ratings.

#### Caution

All adjustments have been done at the factory before the shipment of the devices. No maintenance and user serviceable part is required. Tampering with and modifying the performance of the device will result in voided product warranty.

#### Required Mark

Class 1 Laser Product Complies with 21 CFR 1040.10 and 1040.11

Note: All information contained in this document is subject to change without notice

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